



## 2<sup>nd</sup> Seminar on Electronics Under Harsh Environment

The ETSI (US) Electronic Technology Department, in collaboration with the ALTER-US Cátedra, has organized this first seminar within the Seville University to discuss several topics on the negative effect of the harsh environments into the different electronics. This year we will focus the attention on several radiation effects aspects in different scenarios, research, high energy physics and space.

### Date:

Tuesday 5 November 2019 (16:30- 18:30).

### Location:

Sala de Juntas, Planta ático  
Escuela Técnica Superior de Ingeniería (Universidad de Sevilla).  
Camino de los Descubrimientos, s/n. 41092 Sevilla

### Programme

- **Dan Friedlander**  
Ex Head of Components Engineering. MBT/Israeli Aerospace Industries.  
Space Radiation Hazards, Radiation Hardness Assurance.
  
- **Rogelio Palomo**  
Universidad de Sevilla.  
Radiation hardening by design
  
- **Gonzalo Fernández**  
Technical Advisor and Procurement Quality Director. Alter Technology  
Cátedra Alter-US.

Free admission until complete seats.

